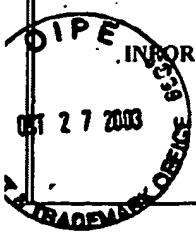


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		APPLICANT Oğuz Tanrikulu					
		FILING DATE March 19, 2001	GROUP 2631				
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EXAM- INER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	AA				RECEIVED		
	AB				JUL 31 2001		
	AC				Technology Center 2600		
	AD						
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	AF						
FOREIGN PATENT DOCUMENTS							
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	AL						
	AM						
	AN						
	AO						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
<i>Jeff</i>	AR	Mitra, S.K. and Hirano, K., "Digital All-Pass Networks," IEEE Trans. Circuits Systs., CAS-21(5): 688-700 (1974) <i>12/13/74</i>					
<i>Jeff</i>	AS	Tanrikulu, O. and Kalkan, M., "Design and Discrete Re-optimization of All-pass Based Power Symmetric IIR Filters," Electronics Letters, 32(16): 1458-1460 (1996) <i>8/96 NO Day</i>					
<i>Jeff</i>	AT	Tanrikulu, O., "Adaptive Signal Processing Algorithms with Accelerated Convergence and Noise Immunity," Ph.D. Thesis, Imperial College of Science, Technology and Medicine, University of London (1995) <i>6/95 NO Day</i>					
<i>Jeff</i>	AU	Valenzuela, R.A and Constantinides, A.G., "Digital Signal Processing Schemes for Efficient Interpolation and Decimation," IEE Proceedings-G, Circuits Devices and Systems, 130(6): 225-235 (1983) <i>12/83 NO Day</i>					
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2644

October 22, 2003

(Use several sheets if necessary)

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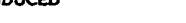
EXAMINER <i>Tiffey Ward</i>	DATE CONSIDERED <i>11/28/03</i>
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INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>SEP 05 2003</i> <i>September 3, 2003</i> <i>(use several sheets if necessary)</i>		APPLICANT Oğuz Tanrikulu	
		FILING DATE March 19, 2001	CONFIRMATION NO. 9012
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TM	AA	5,119,322	02 JUN 1992	Stroobach
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EXAMINER <i>Tiffey, Clarend</i>	DATE CONSIDERED <i>11/28/03</i>
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	APPLICANT Oğuz Tanrikulu	
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EXAMINER <i>Jeffrey Harstel</i>	DATE CONSIDERED <i>11/28/03</i>
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